|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC |  |
| FFFF FFC0 |  |
| FFFF FFC4 |  |
| FFFF FFC8 |  |
| FFFF FFCC |  |
| FFFF FFD0 |  |
| FFFF FFD4 |  |
| FFFF FFD8 |  |
| FFFF FFDC |  |
| FFFF FFE0 |  |
| FFFF FFE4 |  |
| FFFF FFE8 |  |
| FFFF FFEC |  |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 |  |
| FFFF FFFC |  |

Instruction: 92 mov sp, #0

The address that **SP** points to

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC |  |
| FFFF FFC0 |  |
| FFFF FFC4 |  |
| FFFF FFC8 |  |
| FFFF FFCC |  |
| FFFF FFD0 |  |
| FFFF FFD4 |  |
| FFFF FFD8 |  |
| FFFF FFDC |  |
| FFFF FFE0 |  |
| FFFF FFE4 |  |
| FFFF FFE8 |  |
| FFFF FFEC |  |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 104 push {fp}

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC |  |
| FFFF FFC0 |  |
| FFFF FFC4 |  |
| FFFF FFC8 |  |
| FFFF FFCC |  |
| FFFF FFD0 |  |
| FFFF FFD4 |  |
| FFFF FFD8 |  |
| FFFF FFDC |  |
| FFFF FFE0 |  |
| FFFF FFE4 |  |
| FFFF FFE8 |  |
| FFFF FFEC |  |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 |  |
| FFFF FFFC |  |

Instruction: 96 mov fp, 0xffffffC0

The address that **FP** points to

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC |  |
| FFFF FFC0 |  |
| FFFF FFC4 |  |
| FFFF FFC8 |  |
| FFFF FFCC |  |
| FFFF FFD0 |  |
| FFFF FFD4 |  |
| FFFF FFD8 |  |
| FFFF FFDC |  |
| FFFF FFE0 |  |
| FFFF FFE4 |  |
| FFFF FFE8 |  |
| FFFF FFEC |  |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 108 add fp, sp, #4

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC |  |
| FFFF FFC0 |  |
| FFFF FFC4 |  |
| FFFF FFC8 |  |
| FFFF FFCC |  |
| FFFF FFD0 |  |
| FFFF FFD4 |  |
| FFFF FFD8 |  |
| FFFF FFDC |  |
| FFFF FFE0 |  |
| FFFF FFE4 |  |
| FFFF FFE8 |  |
| FFFF FFEC |  |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 |  |
| FFFF FFFC | 148 |

Instruction: 100 push {lr}

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC |  |
| FFFF FFC0 |  |
| FFFF FFC4 |  |
| FFFF FFC8 |  |
| FFFF FFCC |  |
| FFFF FFD0 |  |
| FFFF FFD4 |  |
| FFFF FFD8 |  |
| FFFF FFDC |  |
| FFFF FFE0 |  |
| FFFF FFE4 |  |
| FFFF FFE8 |  |
| FFFF FFEC |  |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 112 sub sp, sp, #8

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC |  |
| FFFF FFC0 |  |
| FFFF FFC4 |  |
| FFFF FFC8 |  |
| FFFF FFCC |  |
| FFFF FFD0 |  |
| FFFF FFD4 |  |
| FFFF FFD8 |  |
| FFFF FFDC |  |
| FFFF FFE0 |  |
| FFFF FFE4 |  |
| FFFF FFE8 |  |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 4 push {lr}

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC |  |
| FFFF FFC0 |  |
| FFFF FFC4 |  |
| FFFF FFC8 |  |
| FFFF FFCC |  |
| FFFF FFD0 |  |
| FFFF FFD4 |  |
| FFFF FFD8 |  |
| FFFF FFDC |  |
| FFFF FFE0 |  |
| FFFF FFE4 |  |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 16 sub sp, sp, #8

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC |  |
| FFFF FFC0 |  |
| FFFF FFC4 |  |
| FFFF FFC8 |  |
| FFFF FFCC |  |
| FFFF FFD0 |  |
| FFFF FFD4 |  |
| FFFF FFD8 |  |
| FFFF FFDC |  |
| FFFF FFE0 |  |
| FFFF FFE4 |  |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 8 push {fp}

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC |  |
| FFFF FFC0 |  |
| FFFF FFC4 |  |
| FFFF FFC8 |  |
| FFFF FFCC |  |
| FFFF FFD0 |  |
| FFFF FFD4 |  |
| FFFF FFD8 |  |
| FFFF FFDC |  |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 20 str r0, [fp, #-8]

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC |  |
| FFFF FFC0 |  |
| FFFF FFC4 |  |
| FFFF FFC8 |  |
| FFFF FFCC |  |
| FFFF FFD0 |  |
| FFFF FFD4 |  |
| FFFF FFD8 |  |
| FFFF FFDC |  |
| FFFF FFE0 |  |
| FFFF FFE4 |  |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 12 add fp, sp, #4

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC |  |
| FFFF FFC0 |  |
| FFFF FFC4 |  |
| FFFF FFC8 |  |
| FFFF FFCC |  |
| FFFF FFD0 |  |
| FFFF FFD4 |  |
| FFFF FFD8 |  |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 4 push {lr}

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC |  |
| FFFF FFC0 |  |
| FFFF FFC4 |  |
| FFFF FFC8 |  |
| FFFF FFCC |  |
| FFFF FFD0 |  |
| FFFF FFD4 |  |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 8 push {fp}

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC |  |
| FFFF FFC0 |  |
| FFFF FFC4 |  |
| FFFF FFC8 |  |
| FFFF FFCC |  |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 20 str r0, [fp, #-8]

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC |  |
| FFFF FFC0 |  |
| FFFF FFC4 |  |
| FFFF FFC8 |  |
| FFFF FFCC |  |
| FFFF FFD0 |  |
| FFFF FFD4 |  |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 12 add fp, sp, #4

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC |  |
| FFFF FFC0 |  |
| FFFF FFC4 |  |
| FFFF FFC8 |  |
| FFFF FFCC | 60 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 4 push {lr}

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC |  |
| FFFF FFC0 |  |
| FFFF FFC4 |  |
| FFFF FFC8 |  |
| FFFF FFCC |  |
| FFFF FFD0 |  |
| FFFF FFD4 |  |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 16 sub sp, sp, #8

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC |  |
| FFFF FFC0 |  |
| FFFF FFC4 |  |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 60 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 8 push {fp}

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC |  |
| FFFF FFC0 |  |
| FFFF FFC4 |  |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 60 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 12 add fp, sp, #4

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC | 60 |
| FFFF FFC0 |  |
| FFFF FFC4 | 2 |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 60 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 4 push {lr}

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC |  |
| FFFF FFC0 |  |
| FFFF FFC4 |  |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 60 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 16 sub sp, sp, #8

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 | FFFF FFCC |
| FFFF FFBC | 60 |
| FFFF FFC0 |  |
| FFFF FFC4 | 2 |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 60 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 8 push {fp}

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC |  |
| FFFF FFC0 |  |
| FFFF FFC4 | 2 |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 60 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 20 str r0, [fp, #-8]

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 | FFFF FFCC |
| FFFF FFBC | 60 |
| FFFF FFC0 |  |
| FFFF FFC4 | 2 |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 60 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 12 add fp, sp, #4

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 | FFFF FFCC |
| FFFF FFBC | 60 |
| FFFF FFC0 |  |
| FFFF FFC4 | 2 |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 60 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 16 sub sp, sp, #8

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 | 1 |
| FFFF FFB8 | FFFF FFCC |
| FFFF FFBC | 60 |
| FFFF FFC0 |  |
| FFFF FFC4 | 2 |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 60 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 80 pop {fp}

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 | 1 |
| FFFF FFB8 | FFFF FFCC |
| FFFF FFBC | 60 |
| FFFF FFC0 |  |
| FFFF FFC4 | 2 |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 60 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 20 str r0, [fp, #-8]

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 | 1 |
| FFFF FFB8 | FFFF FFCC |
| FFFF FFBC | 60 |
| FFFF FFC0 |  |
| FFFF FFC4 | 2 |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 60 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 84 pop {pc}

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 | 1 |
| FFFF FFB8 | FFFF FFCC |
| FFFF FFBC | 60 |
| FFFF FFC0 |  |
| FFFF FFC4 | 2 |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 60 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 76 sub sp, fp, #4

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 | 1 |
| FFFF FFB8 | FFFF FFCC |
| FFFF FFBC | 60 |
| FFFF FFC0 |  |
| FFFF FFC4 | 2 |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 60 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 76 sub sp, fp, #4

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 | 1 |
| FFFF FFB8 | FFFF FFCC |
| FFFF FFBC | 60 |
| FFFF FFC0 |  |
| FFFF FFC4 | 2 |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 60 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 80 pop {fp}

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 | 1 |
| FFFF FFB8 | FFFF FFCC |
| FFFF FFBC | 60 |
| FFFF FFC0 |  |
| FFFF FFC4 | 2 |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 120 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 80 pop {fp}

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 | 1 |
| FFFF FFB8 | FFFF FFCC |
| FFFF FFBC | 60 |
| FFFF FFC0 |  |
| FFFF FFC4 | 2 |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 60 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 84 pop {pc}

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 | 1 |
| FFFF FFB8 | FFFF FFCC |
| FFFF FFBC | 60 |
| FFFF FFC0 |  |
| FFFF FFC4 | 2 |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 120 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 84 pop {pc}

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 | 1 |
| FFFF FFB8 | FFFF FFCC |
| FFFF FFBC | 60 |
| FFFF FFC0 |  |
| FFFF FFC4 | 2 |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 60 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 76 sub sp, fp, #4

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 | 1 |
| FFFF FFB8 | FFFF FFCC |
| FFFF FFBC | 60 |
| FFFF FFC0 |  |
| FFFF FFC4 | 2 |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 120 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 76 sub sp, fp, #4

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 | 1 |
| FFFF FFB8 | FFFF FFCC |
| FFFF FFBC | 60 |
| FFFF FFC0 |  |
| FFFF FFC4 | 2 |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 60 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction 80 pop {fp}

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 | 1 |
| FFFF FFB8 | FFFF FFCC |
| FFFF FFBC | 60 |
| FFFF FFC0 |  |
| FFFF FFC4 | 2 |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 60 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 136 sub sp, fp, #4

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 | 1 |
| FFFF FFB8 | FFFF FFCC |
| FFFF FFBC | 120 |
| FFFF FFC0 |  |
| FFFF FFC4 | 2 |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 120 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
|  |  |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
|  |  |
|  |  |
|  |  |

Nothing here..

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 | 1 |
| FFFF FFB8 | FFFF FFCC |
| FFFF FFBC | 60 |
| FFFF FFC0 |  |
| FFFF FFC4 | 2 |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 60 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 140 pop {fp}

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 | 1 |
| FFFF FFB8 | FFFF FFCC |
| FFFF FFBC | 60 |
| FFFF FFC0 |  |
| FFFF FFC4 | 2 |
| FFFF FFC8 | FFFF FFDC |
| FFFF FFCC | 60 |
| FFFF FFD0 |  |
| FFFF FFD4 | 3 |
| FFFF FFD8 | FFFF FFEC |
| FFFF FFDC | 60 |
| FFFF FFE0 |  |
| FFFF FFE4 | 4 |
| FFFF FFE8 | FFFF FFFC |
| FFFF FFEC | 124 |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 | 0xFFFFFFFC0 |
| FFFF FFFC | 148 |

Instruction: 124 str r0, [fp, #-8]

|  |  |
| --- | --- |
| Address | Value |
| FFFF FFA0 |  |
| FFFF FFA4 |  |
|  |  |
| FFFF FFA8 |  |
| FFFF FFAC |  |
| FFFF FFB0 |  |
| FFFF FFB4 |  |
| FFFF FFB8 |  |
| FFFF FFBC |  |
| FFFF FFC0 |  |
| FFFF FFC4 |  |
| FFFF FFC8 |  |
| FFFF FFCC |  |
| FFFF FFD0 |  |
| FFFF FFD4 |  |
| FFFF FFD8 |  |
| FFFF FFDC |  |
| FFFF FFE0 |  |
| FFFF FFE4 |  |
| FFFF FFE8 |  |
| FFFF FFEC |  |
| FFFF FFF0 |  |
| FFFF FFF4 |  |
| FFFF FFF8 |  |
| FFFF FFFC | 148 |

Register Contents

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| r0 | 4 | 3 | 2 | 1 | 3 | 6 | 10 | 0 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| r2 | 1 | 3 | 6 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| r3 | 4 | 3 | 2 | 1 | 2 | 3 | 6 | 4 | 10 | 0 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| lr | 148 | 124 | 60 | 60 | 60 | 60 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| sp | 0x0000 0000 | 0xFFFF FFFC | 0xFFFF FFF8 | 0xFFFF FFF0 | 0xFFFF FFEC | 0xFFFF FFE8 |
| sp | 0xFFFF FFE0 | 0xFFFF FFDC | 0xFFFF FFD8 | 0xFFFF FFD0 | 0xFFFF FFCC | 0xFFFF FFC8 |
| sp | 0xFFFF FFC0 | 0xFFFF FFBC | 0xFFFF FFB8 | 0xFFFF FFB0 | 0xFFFF FFB8 | 0xFFFF FFBC |
| sp | 0xFFFF FFC0 | 0xFFFF FFC8 | 0xFFFF FFCC | 0xFFFF FFD0 | 0xFFFF FFD8 | 0xFFFF FFDC |
| sp | 0xFFFF FFE0 | 0xFFFF FFE8 | 0xFFFF FFEC | 0xFFFF FFF0 | 0xFFFF FFF8 | 0xFFFF FFFC |
| sp | 0x0000 0000 |  |  |  |  |  |
| sp |  |  |  |  |  |  |

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| fp | 0x0000 C000 | 0xFFFF FFFC | 0xFFFF FFEC | 0xFFFF FFDC | 0xFFFF FFCC | 0xFFFF FFBC |
| fp | 0xFFFF FFCC | 0xFFFF FFDC | 0xFFFF FFEC | 0xFFFF FFFC |  |  |
| fp |  |  |  |  |  |  |
| fp |  |  |  |  |  |  |
| fp |  |  |  |  |  |  |
| fp |  |  |  |  |  |  |
| fp |  |  |  |  |  |  |